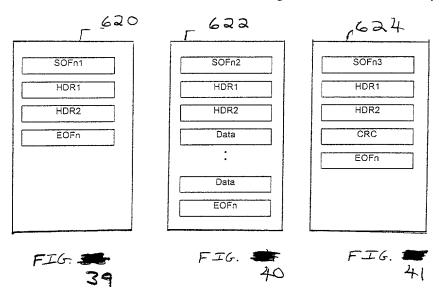
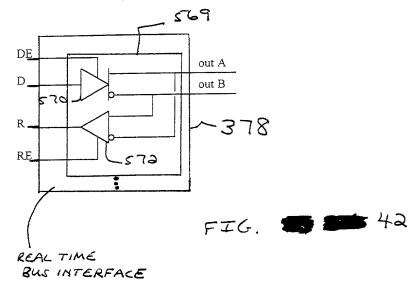


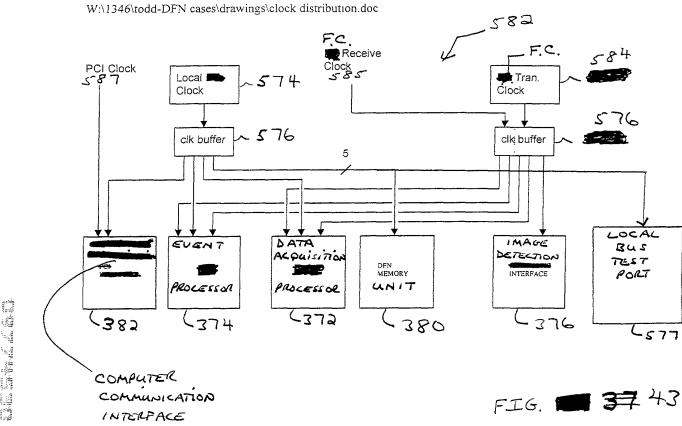
\\NTSERVFR\WORK\1346\todd-DFN cases\drawings\fibre channel transmission protocol.doc



\\NTSERVER\WORK\1346\todd-DFN cases\drawings\rs-485 transceiver channel.doc



1-2



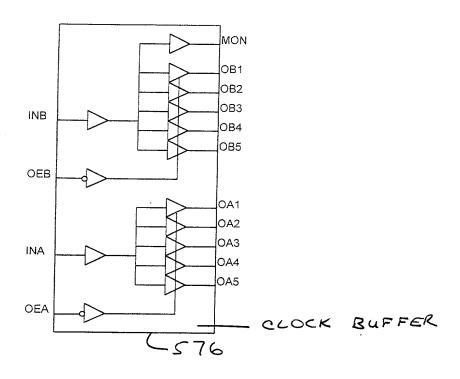
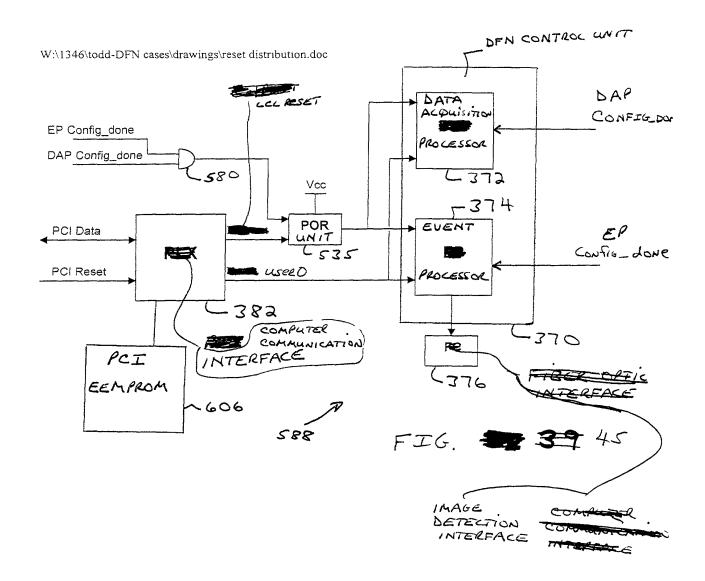
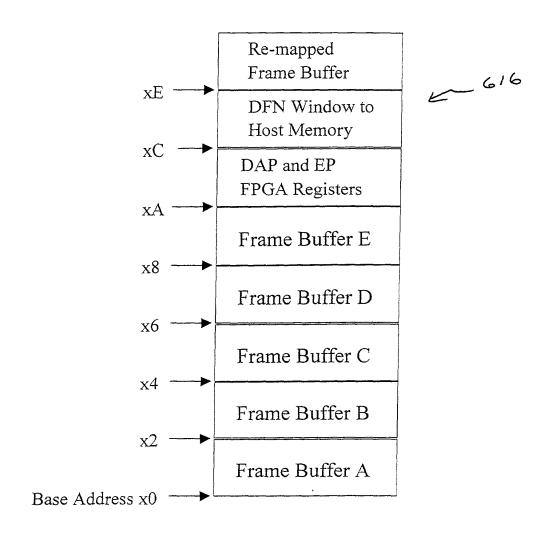


FIG. = \$44



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·)



Mapping of 16 MByte PCI Address Space

FIG. 每 47

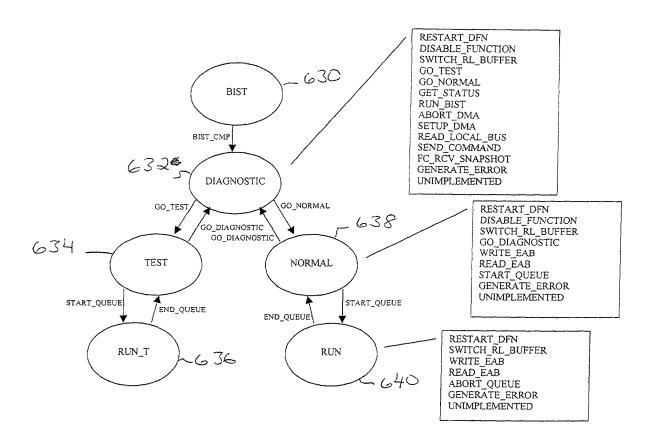
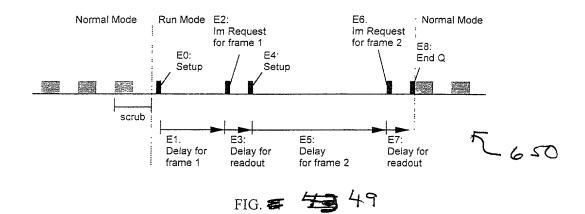
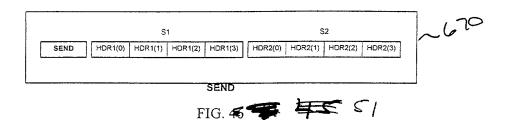


FIG. #248



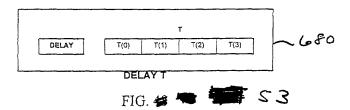
Event Mnemonic	Event (showing size of arguments)	Op Code (hex)	Data (bytes)	Total (bytes)
Endq	Endq	14	0	
Delay (T)	Delay (0xff ff ff ff)	10	4	5
Send (command, value)	Send (0xff ff ff ff, 0xff ff ff ff)	04	8	9
LoopKN (K, N)	LoopKN (0xff ff, 0xff)	0C	3	4
LoopKF (K, F)	LoopKF (0xff ff, 0xff ff ff)	0D	5	6
Wait (F)	Wait (0xff ff ff)	09	3	4
Flag (F)	Flag (0xff ff ff)	08	3	4

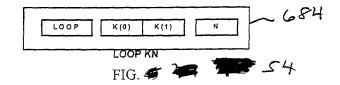
FIG. # # 50



Error Mnemonic	Description of Error]
FC_TIMEOUT	Timeout expired with no ACK detected	1
FC_BAD_ACK	ACK did not match transmitted command	
FC_EXTRA_ACK	Unexpected ACK received	1
FC_EXTRA_CMD	New Send event while waiting for ACK from previous Send	
SIG_DETN	No input signal power on Fibre Channel (cable disconnected?)	£673
RXERROR	Fibre Channel receiver detected bad data (defective chipset?)	
WRDSYNCN	Fibre Channel Data link unsynchronized	
CRXS(1)	Bad Received CRC detected (Fiber-optic cable problem?)	
CRXS(3) and CRXS(2)	Bad order in link state machine (defective chipset?)	

FIG. 4 5 2





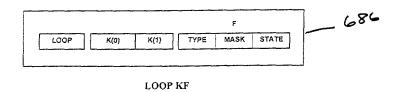
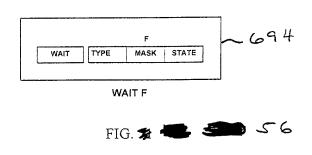
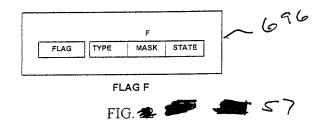


FIG. 99 19 55









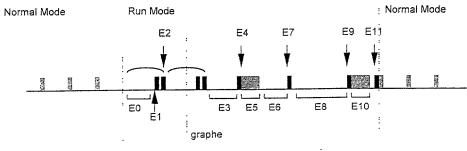
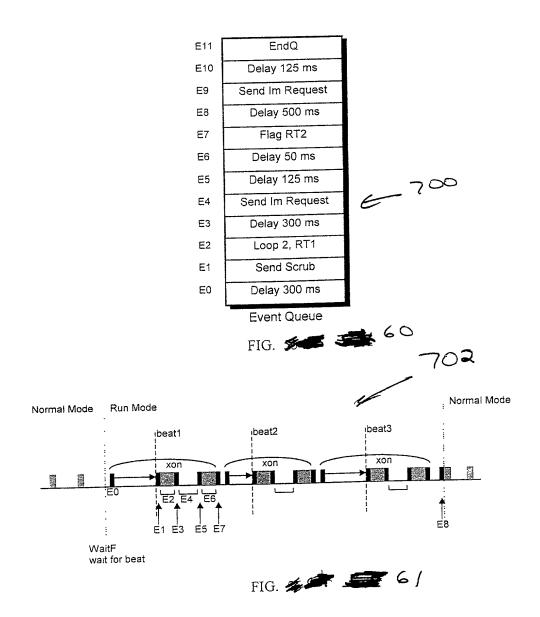
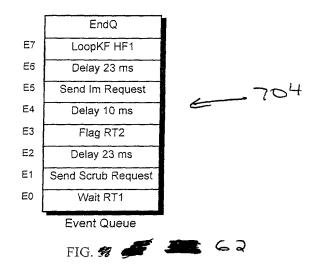
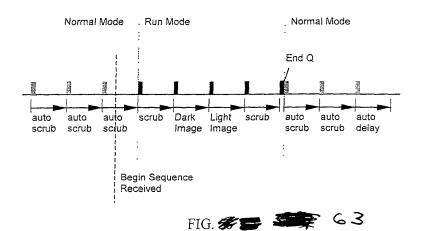


FIG # 4 59







```
sequence_begin ();

# define qv defaults:
%qv1 =('delay_qv' => 5000);

# call frame with qv's
frame_type1 (NULL, \%qv1, 1);
sequence_end ();
```

```
sub frame
{
    $QVf = 'frame';
    %qv = ('delay_qv' => [10000]);
    %qp = ();
    compile_init(@_,\%qp,\%qv,SQVf);
    Delay('delay_qv');
    compile_finit();
}
```

FIG. # 64

FIG. 🗶 65

```
pDFN->DFNChangeQueueVariable

(char *)SymName, // variable name
(char *)sndBuf, // new value
BufSize, // num bytes to write
(ULONG *)&debug // developer info
);
```

FIG. @ 66

```
User Application
```

```
Perl Script
```

```
sub frame_type1
{
    $HFfrm = 'frame_type1';
    $qv = ('delay_qv' => [20000] );
    $qp = ();

    $image_cmd = [0x800000,0x0];

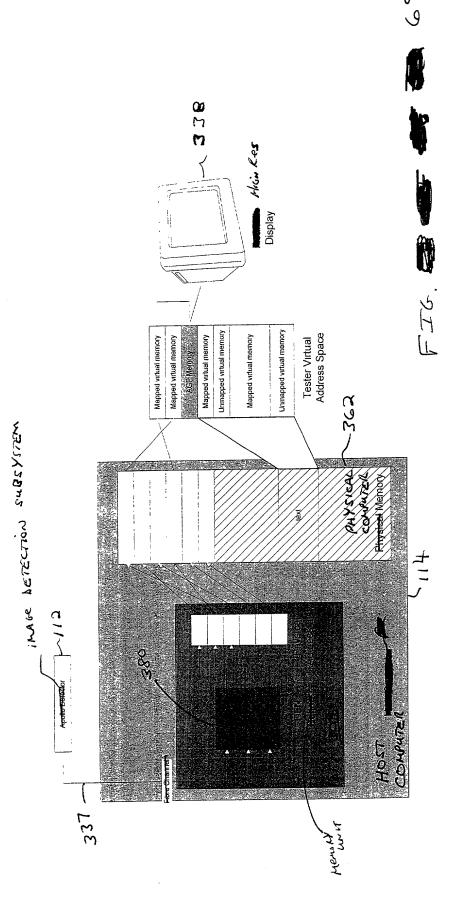
    compile_init(@_,\%qp,\%qv,$HFfrm);

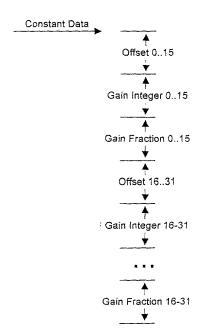
    Send($image_cmd);
    Delay('delay_qv');
    LoopKF(2,0xAAFF01);

compile_finit();
}
```

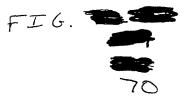
FIG. 67

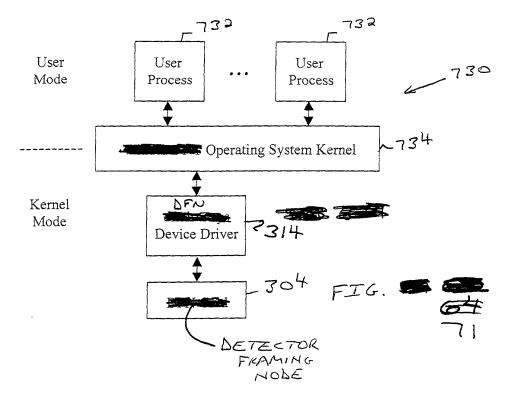
FIG. 68

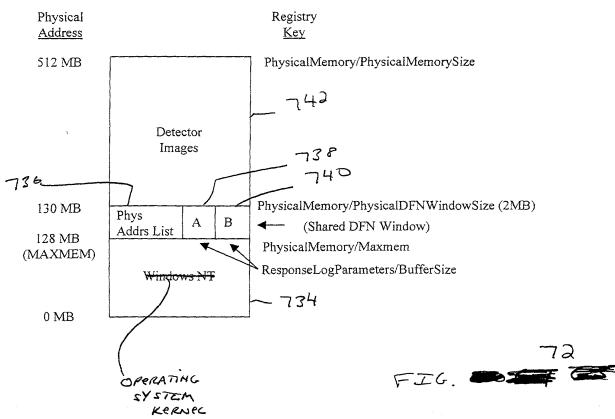




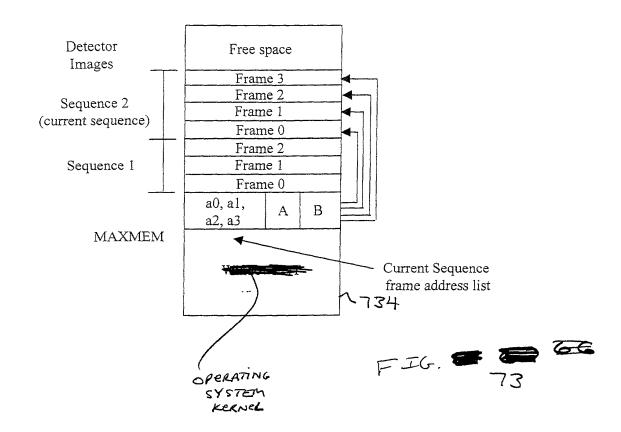
Constant Memory Format

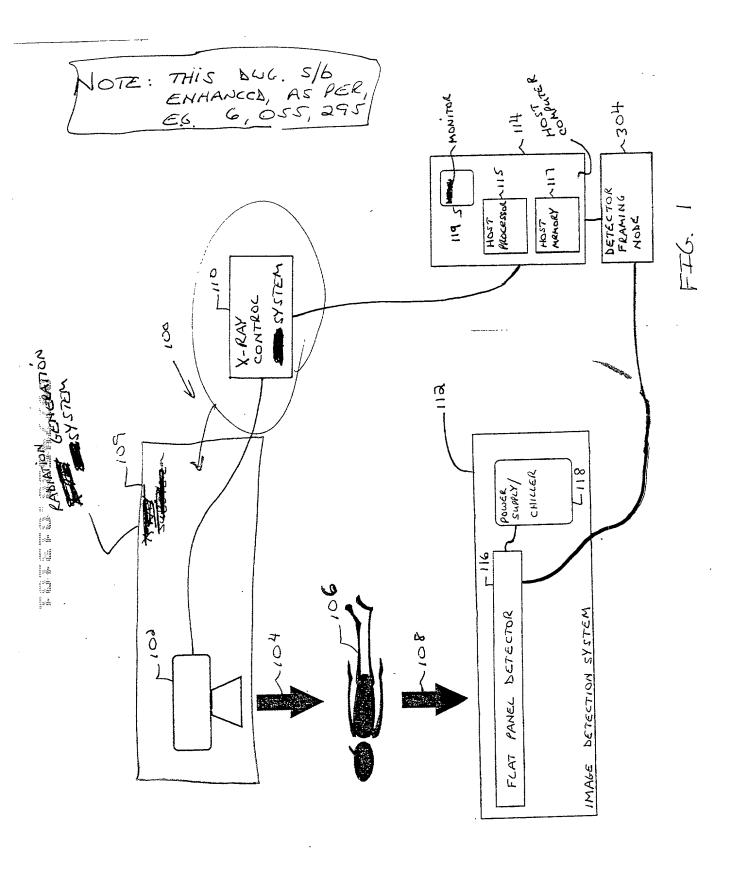


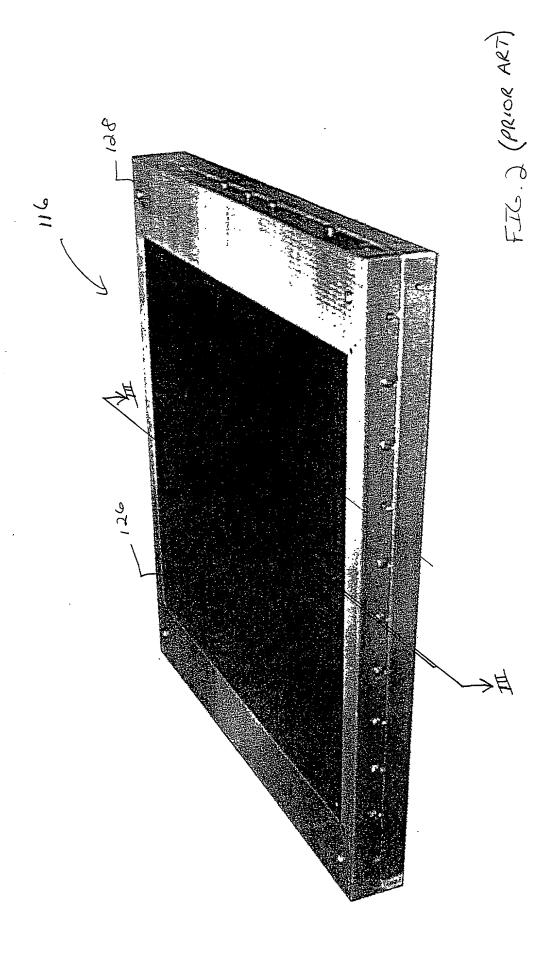


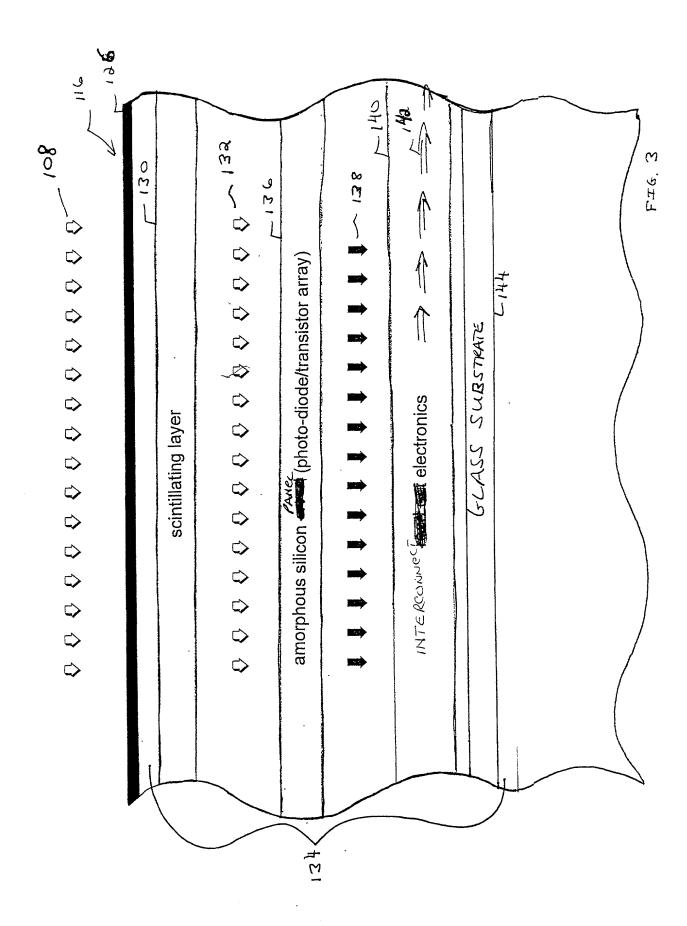


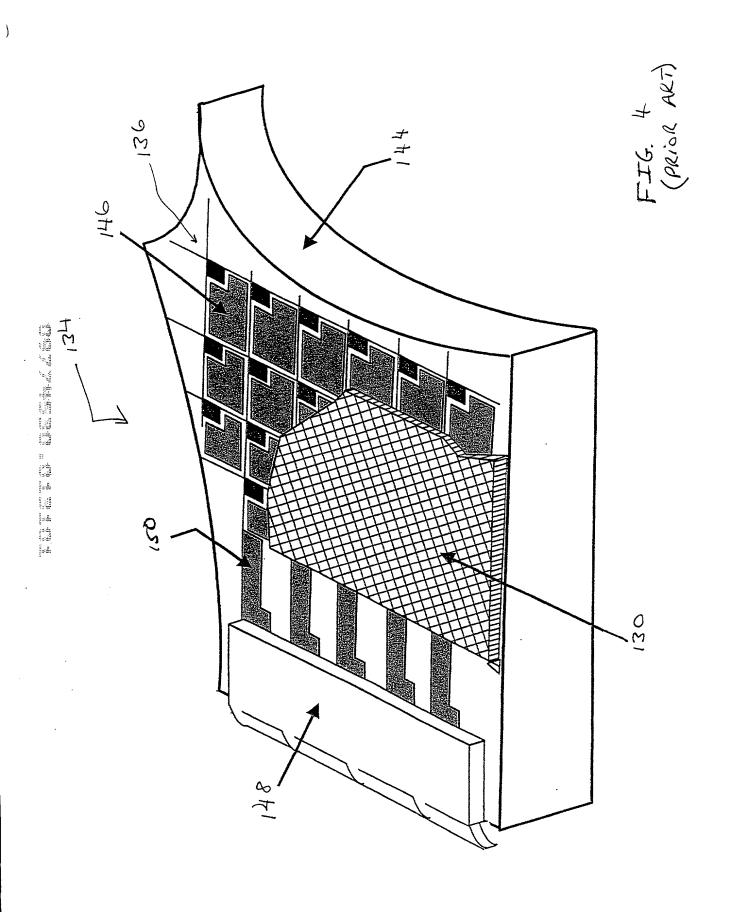












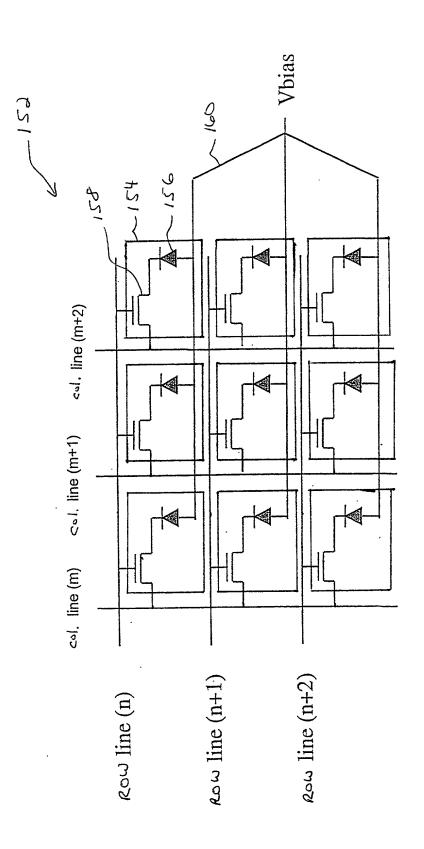
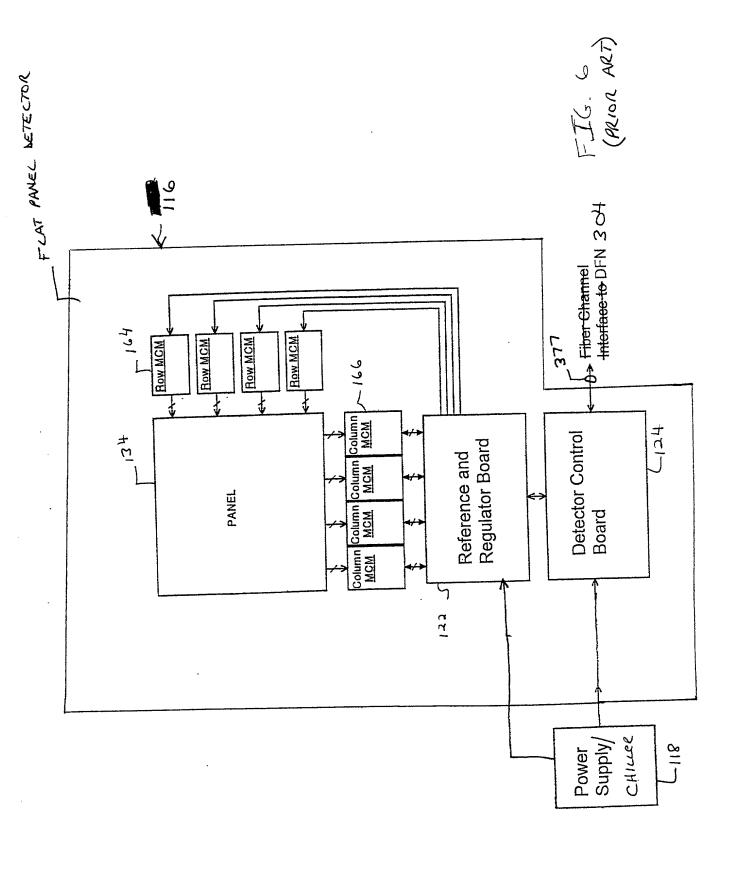
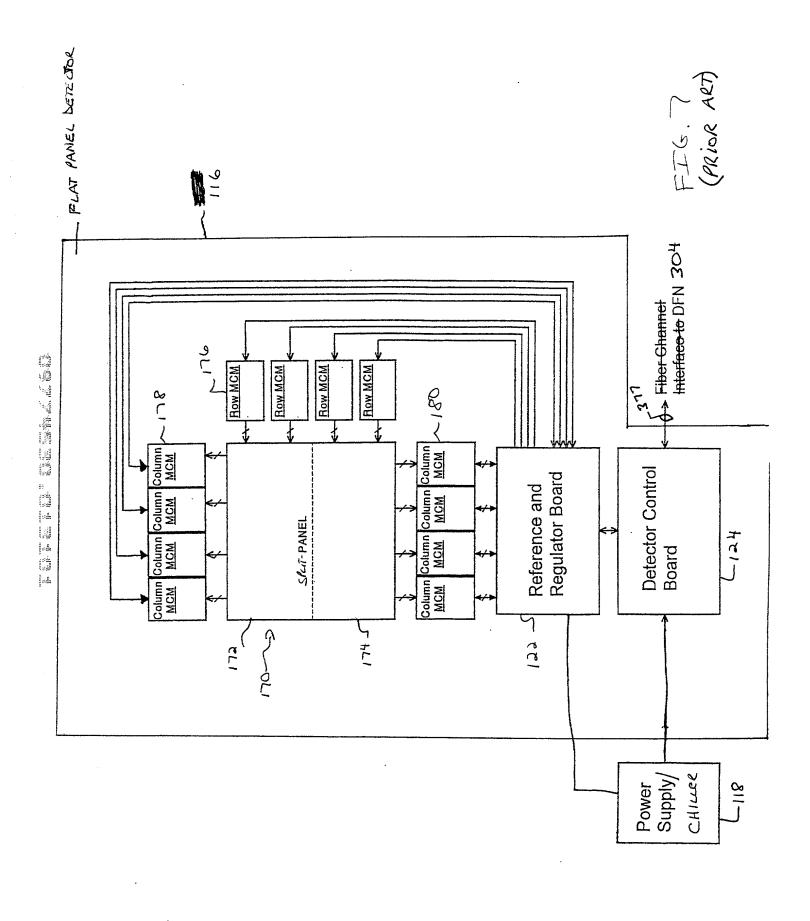
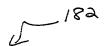
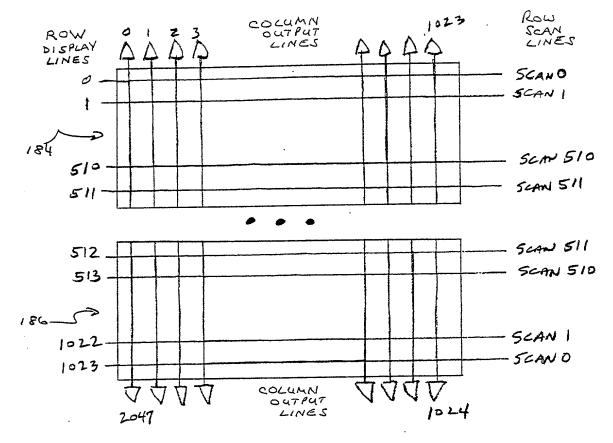


FIG. S (PLIOR ART)



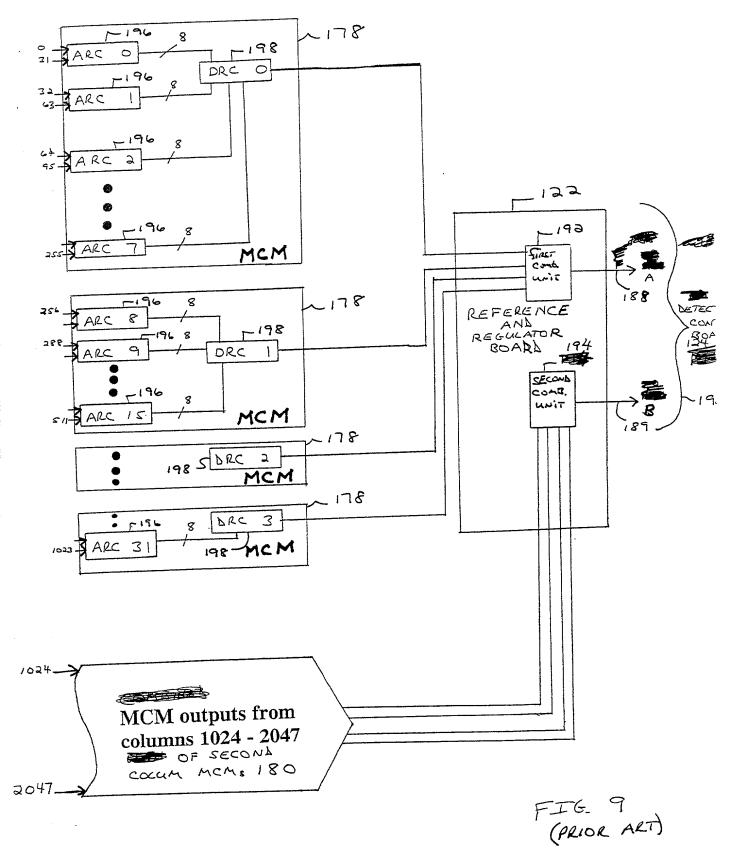


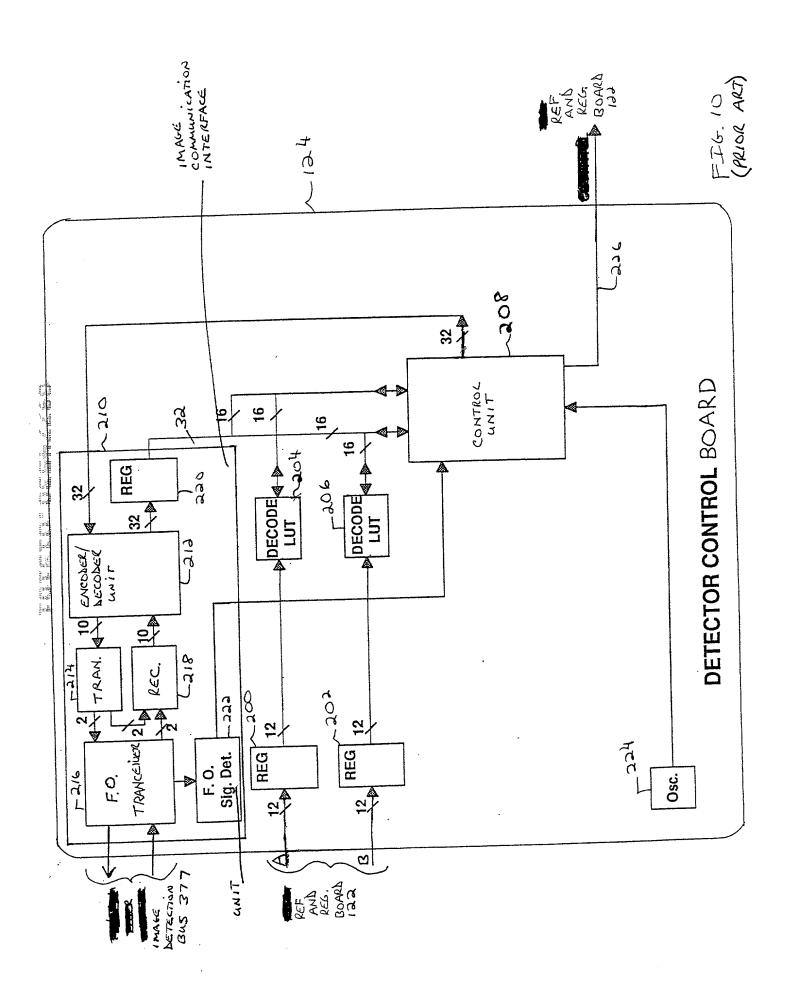


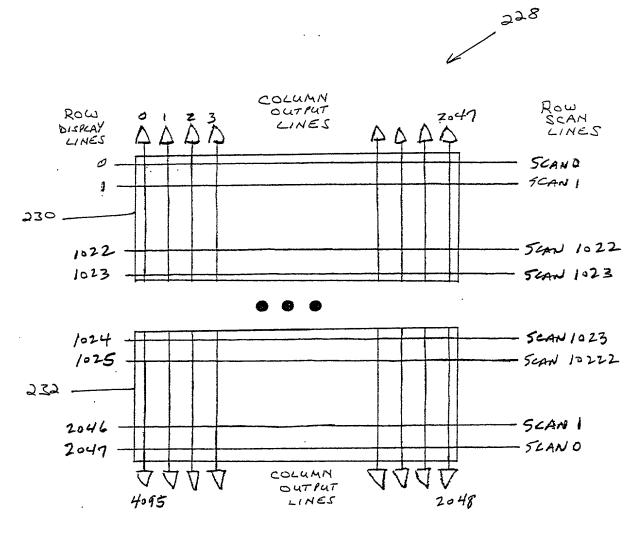


CARDIAC/SURGICAL DIGITAL X-RAY PANEL

FIG. 8 (PRIOR ART)

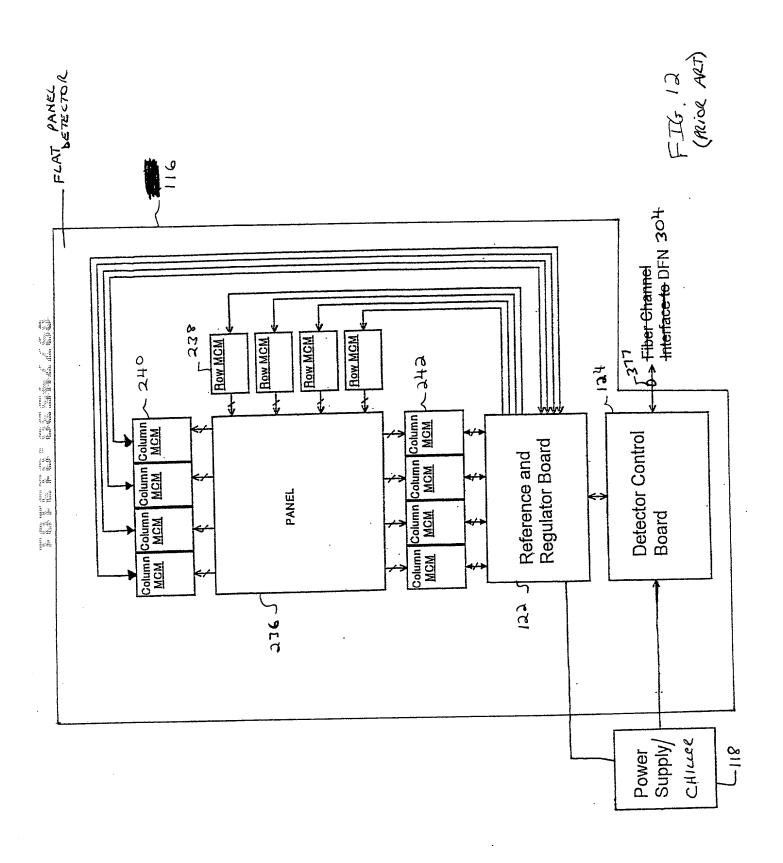


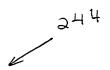


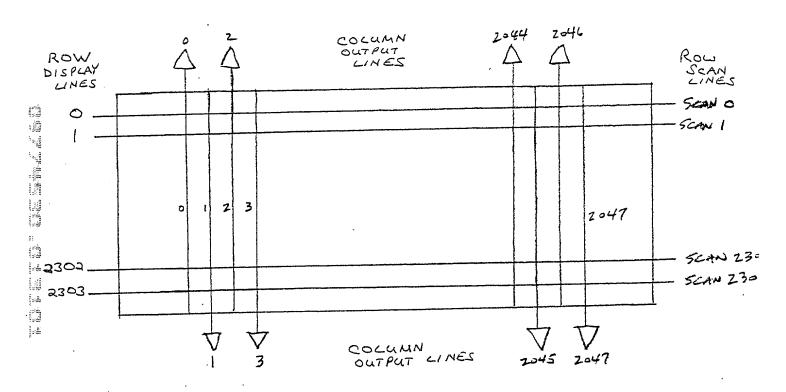


RADIOGRAPHY DIGITAL X-RAY PANEL

FIG. 11 (PRIOR ART)

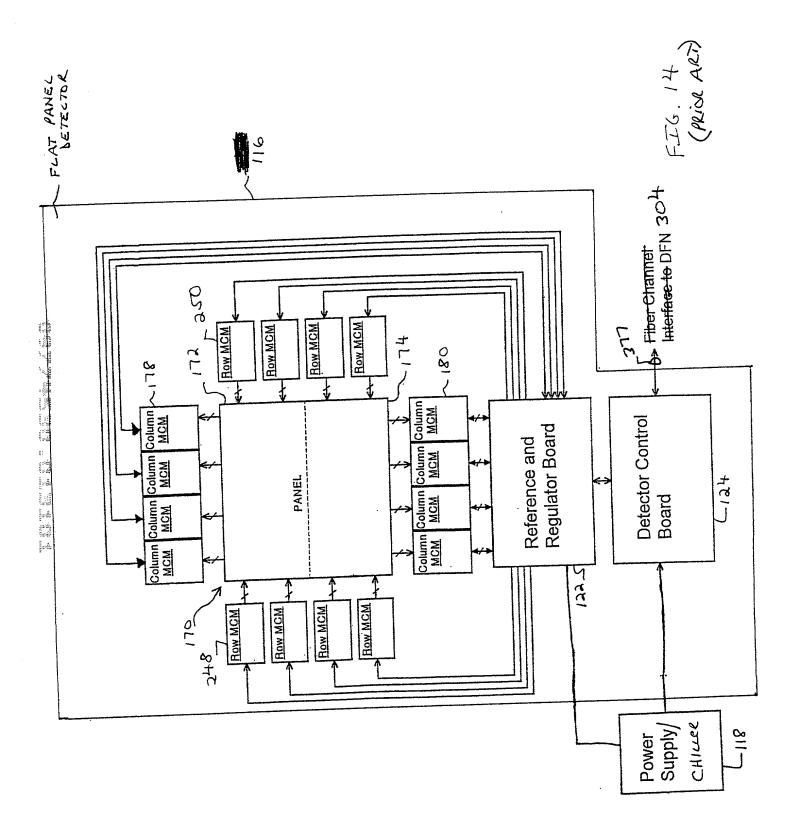


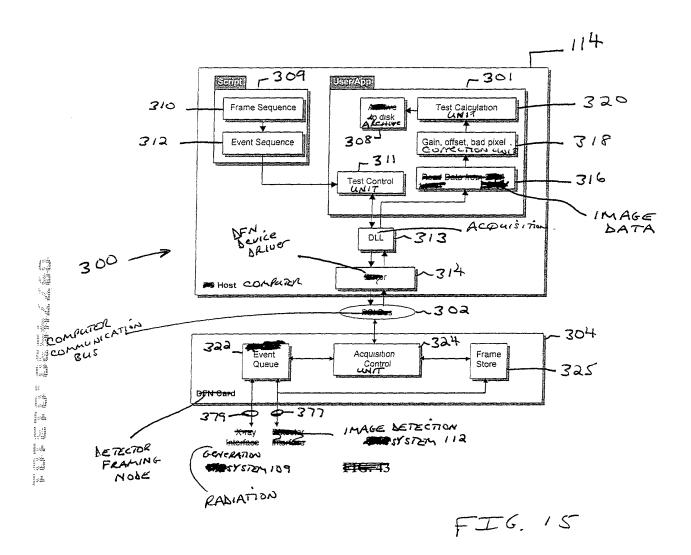




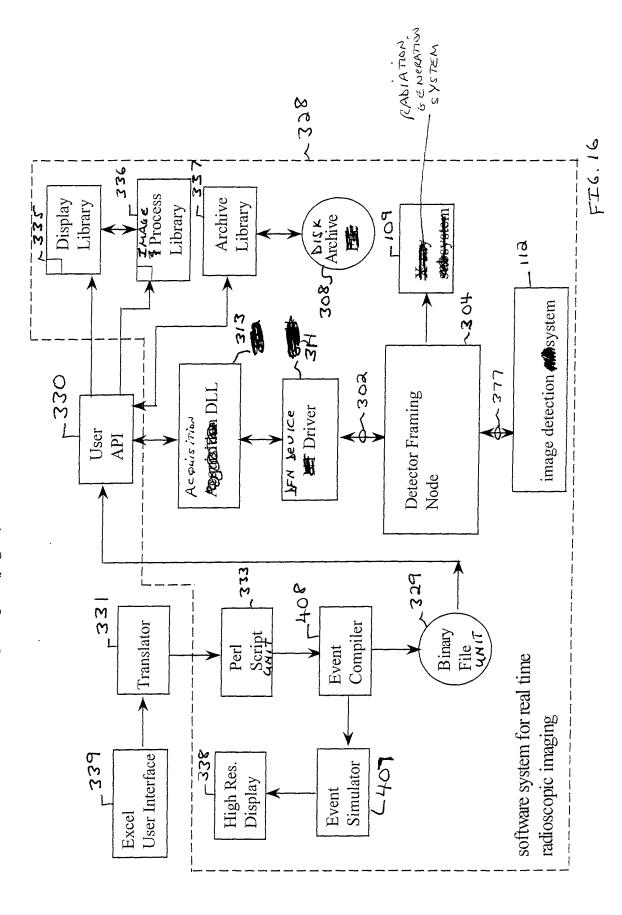
MAMOGRAPHY DIGITAL X-RAY PANEL

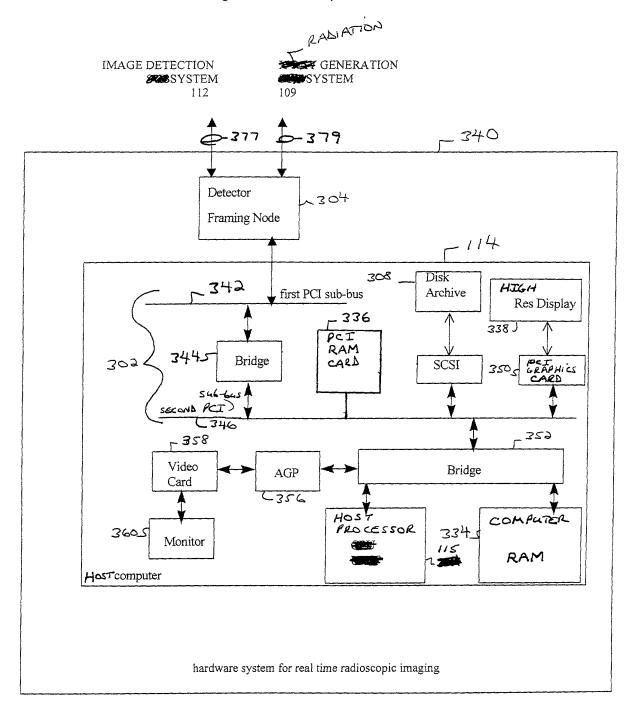
FIG. 13 (PRIOR ART)

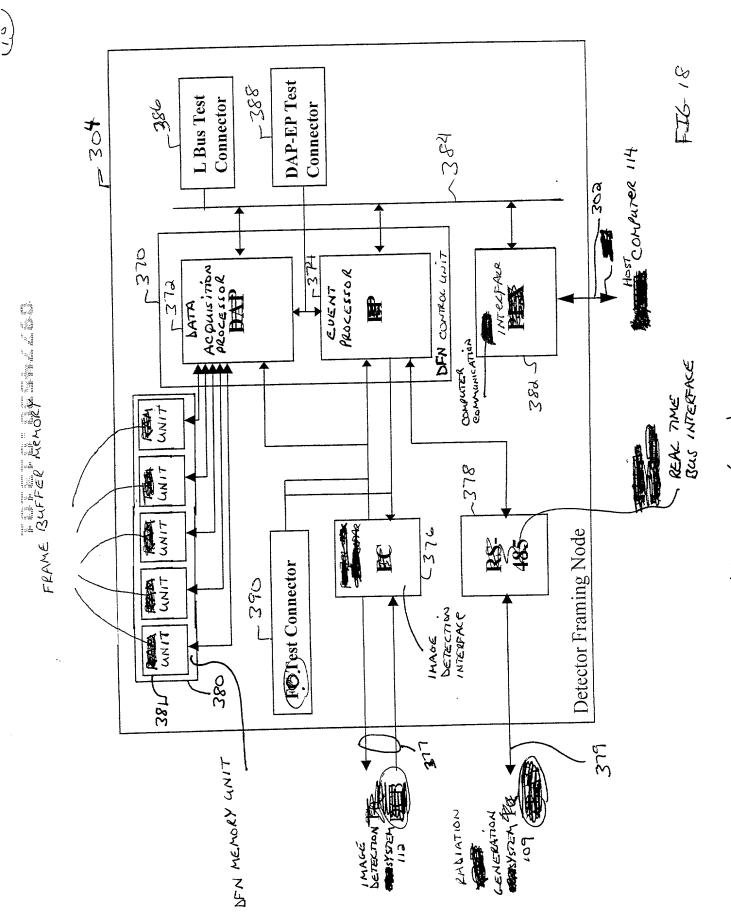




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(11.11) The Tobe NEW CASES DRAWINGS | BUG-01 (PAGE 2)

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memory offset gbr	host "	n os t
Latency	5 frames Delay ~.1 sec Delay ~ 2 sec	< 5 frames < 5 frames < 5 frames
length	rillin ited	Unlim ited Unlim ited Unlim ited
(fm/sec) length) i i	ж ж × >
Real Time Post Process Post Process Real Time Real Time		
Panel Setup	Single Frame Single Frame	Real Time Real Time Real Time

 Modality
 Image size
 Frames Stored

 Cardiac
 1024 x 1024
 200

 Rad
 2048 x 2048
 50

 Mammo
 2304 x 2048
 44

F1620

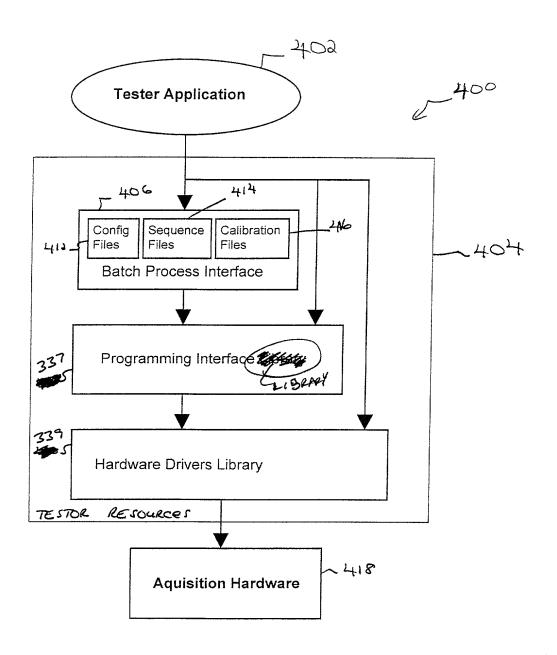
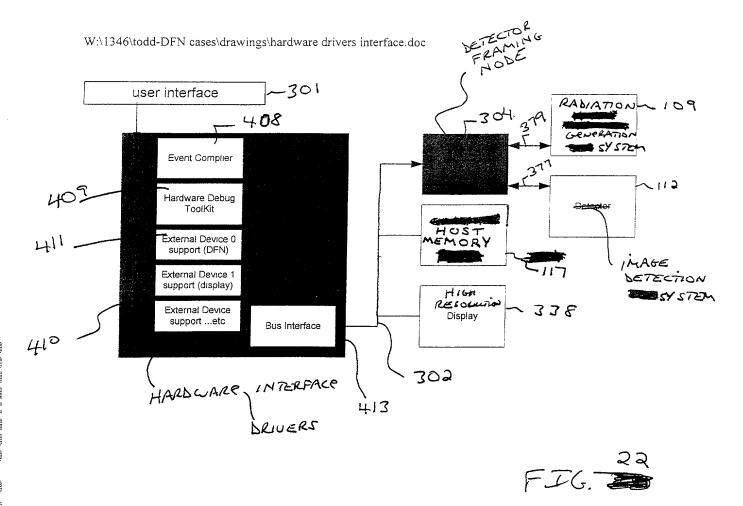


FIG. #21



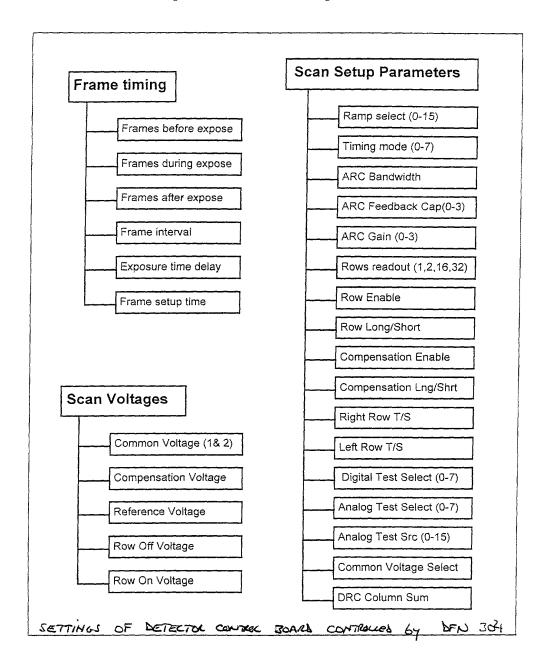


FIG. 23

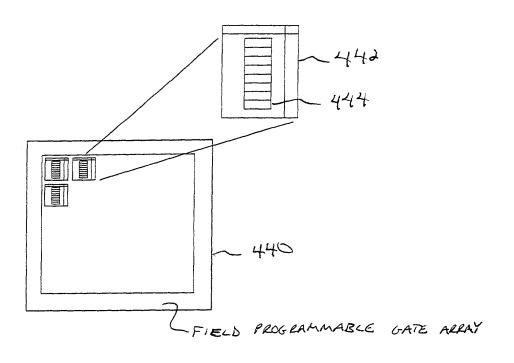


FIG. 234

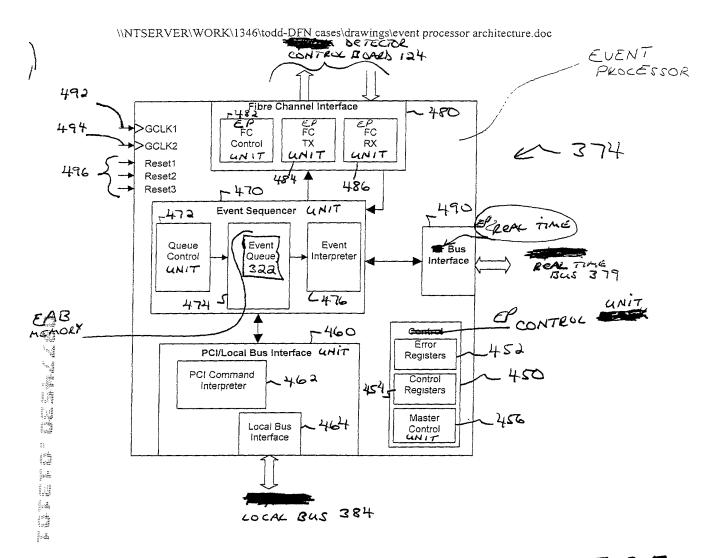


FIG. # 25

